



PTMC core processing platform with PICMG 2.16 support

Product #OSS-HW400c/M



Product Features

- MPC8245 PowerPC processor, 333 MHz
- Two CT Bus enabled PTMC sites
- Mix and match PMC and PTMC
- Dual node 10/100 Ethernet (PICMG 2.16 cPSB)
- H.110 CT Bus interface (4096 timeslots)

Ordering Information

Intelligent carrier with cPSB, 2 PTMC sites, H.110

Product #OSS-HW400c/M

HW400c/M without H.110

Product #OSS-HW400c/M-LC

HW400c/M developers rear transition module

Product #OSS-HW400c/M-DTM

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Call Toll Free! 1-877-438-2724

Specifications

System Processor

- Motorola MPC8245 PowerPC, 333 MHz

Memory

- SDRAM: 128 MB
- Flash: 1 MB (socketed), 32 MB (soldered)
- NVRAM - 2 KB

PTMC Sites

- 2x PICMG 2.15, PT2MC - 3.3V or 5V signaling
- PICMG 2.3 Rear I/O Mapping

PSB/Ethernet

- 2x 10/100 Ethernet cPSB - PICMG 2.16 Single or Dual Node
- Selectable Front Panel or PSB

H.110 Interface

- PICMG 2.5 CT Bus Interface
- 4096 Timeslot H.110
- 2560 Timeslot Local CT Bus (PT2MC)

CompactPCI Interface

- 32/64-bit, 33/66 MHz PCI 2.2
- PICMG 2.1 R2.0 Full Hot Swap
- CompactPCI 6U x 4HP (single slot)

Power Requirements

- +5 VDC @ approx. 600 mA core
- +3.3 VDC @ approx. 1.5 A core
- +12 VDC @ approx. 45 mA core
- -12 VDC @ approx. 1 mA core
- PTMC sites @ up to 7.5 W per site

Environmental - Operating

- Temperature: -5 to 55°C (23 to 131°F)
- Humidity: 5 to 90% non-condensing

Environmental - Storage

- Temperature: -40 to 70°C (-40 to 158°F)
- Humidity: 0 to 95% non-condensing

Pending Certifications

- CE, UL, cUL, FCC Class A

Drivers

- Linux



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