

User Manual, PCIe x4 Host Cable Adapter (HIB2)

OSS-PCIe-HIB2-x4-H (Host)
OSS-PCIe-HIB2-x4-T (Target)



www.onestopsystems.com

Phone 877-438-2724 • Fax 760-466-1678 • sales@onestopsystems.com

Table of Contents

1. Overview

1.a. Unpacking Instructions	1
1.b. Product description.....	1
1.c. Specifications	1
1.d. Block diagram.....	2

2. Initial Setup

2.a. Installation and removal	2
2.b. Installing the host cable adapter.....	2
2.c. Connecting PCIe cable.....	3
2.d. Removing PCIe cable.....	3
2.e. Installation instructions for a 2-slot backplane.....	3

3. Pin assignments

5. Ordering Information

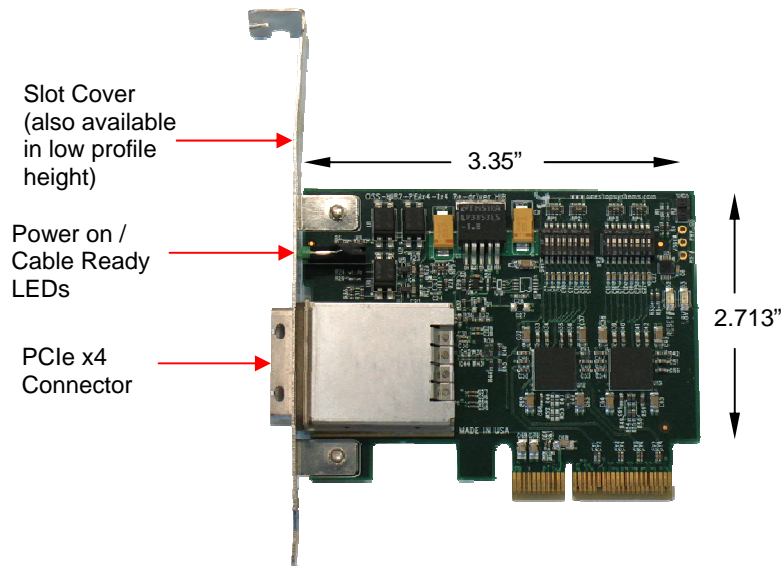
1. Overview

1.a. Unpacking Instructions

- If the carton is damaged when you receive it, request that the carrier's agent be present when you unpack and inspect the equipment.
- After unpacking, verify that all items listed in the packing list are present.
- Inspect the equipment for shipping damage.

1.b. Product Description

The Host Cable Adapter (Part # OSS-PCIe-HIB2-x4-H) allows communication between a processor and an I/O port. The OSS-PCIe-HIB2-x4-T is used as a target in a One Stop Systems 2 slot backplane (OSS-PCIe-BP-2000).



1.c. Specifications

Dimensions (H x L): 2.7 x 3.4 inches (68 x 86mm)

Front Panel Connectors: Power On / Cable Present LEDs

Power Consumption (designed to meet the following conditions): 3.75W typical, 3.3@ 1.3A

Operating Environment (designed to meet the following conditions)

Temperature Range: 0° to 50°C (32° to 122°F)

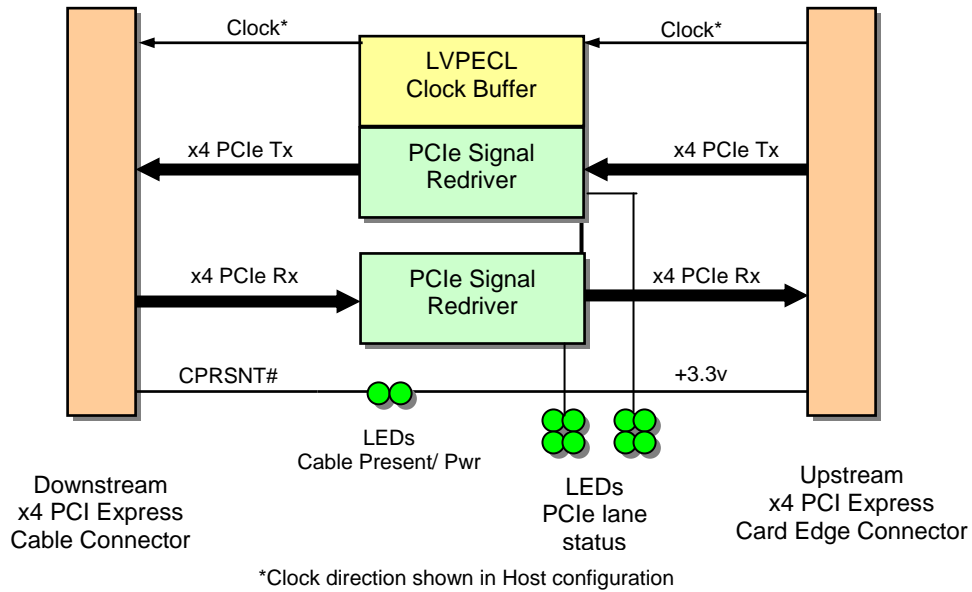
Relative Humidity: 10 to 90% non-condensing

Shock: 30g acceleration peak amplitude displacement; 7-2000Hz, 1.5g acceleration.

Redriver: Pericom PI2EQX4402D

Agency Compliance Designed to meet, but not tested: UL60950, FCC Class A, CE safety and emissions

1.d. Block Diagram



2. Initial Set-Up

2.a. Installation and removal

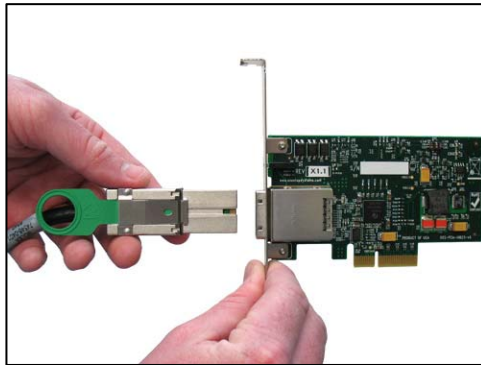
- 1) Power down the host system.
- 2) Open the chassis according to your system documentation.
- 3) Remove the host cable adapter from the protective bag, observing proper ESD safety procedures.

2.b. Installing the host cable adapter:

- 1) Insert the host cable adapter into a PCIe x4, x8 or x16 add-in slot in the host motherboard. Make sure that the card is well seated and tighten the screw on the top of the slot cover.
- 2) Attach the cable by first pulling back on the retractor ring. Align the keyed slot with the connector key ridge on the slot cover. Insert the cable connector into the connector shell on the board **until the connector teeth snap securely into the holes in the cable shell.**
- 3) The connectors on either end of the PCIe x4 cable are identical. Each connector is equipped with a retractor to allow the connector to be locked into place.

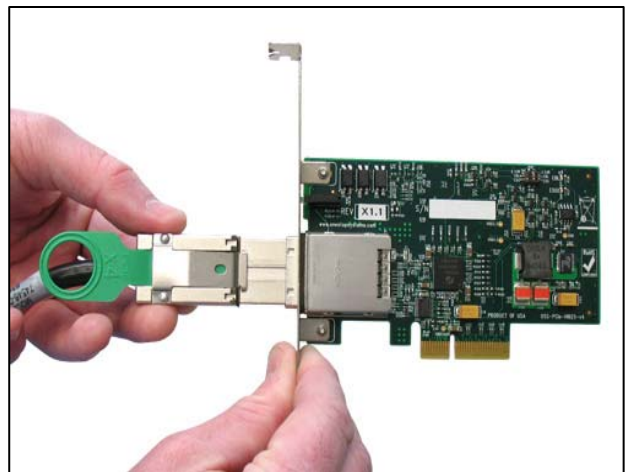
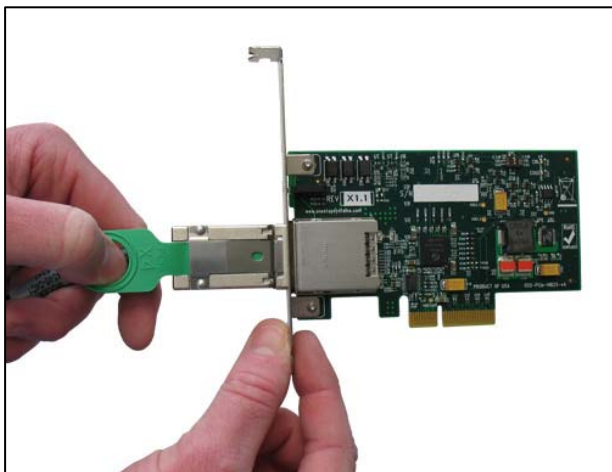
2.c. Connecting PCIe cable

- 1) To install the PCIe cable, Insert into the expansion link board firmly or until you feel it lock into place.



2.d. Removing PCIe cable:

- 2) To remove PCIe cable pull back on green thumb tab to release metal pins and gently separate.



2.e. Installation instructions for a 2-slot backplane

- 1) Insert the HIB2 x4 in host mode in a PCIe slot in the host computer
- 2) Connect the 2-slot backplane to an ATX power supply separate from the host system power supply.
- 3) Insert the HIB2 x4 in target mode in the PCIe connector on the test backplane, next to the ATX connector.
- 4) Insert the PCIe endpoint board in the other PCIe slot on the test backplane.
- 5) Connect the PCIe cable to both the host and target HIB2 x4.
- 6) Turn on the power supply for the test backplane. NOTE: The power supply and test backplane will not power up at this time.
- 7) Turn on the host system. Signals across the PCIe cable will cause the test backplane to power up as well. The power and cable LEDs on both HIB2 x4s should be on, indicating that the host system is communicating with the endpoint board.

3. Pin Assignments

Connectors PCIe x4 Card Edge Connector

- The pins are numbered as shown with side A on the top of the centerline on the solder side of the board and side B on the bottom of the centerline on the component side of the board.
- The PCIe interface pins PETpx, PETnx, PERpx, and PERnx are named with the following convention: “PE” stands for PCIe high speed, “T” for Transmitter, “R” for Receiver, “p” for positive (+), and “n” for negative (-).
- Note that adjacent differential pairs are separated by two ground pins to manage the connector crosstalk.

Table 1: Pin-out for the PCIe x4 Card Edge Connector on the Host Cable Adapter

Pin #	Side B		Side A	
	Name	Description	Name	Description
1	N/C	N/C	PRSNT1#	Hot-Plug presence detect
2	N/C	N/C	N/C	N/C
3	N/C	N/C	N/C	N/C
4	GND	Ground	GND	Ground
5	NC	N/C	N/C	Not connected
6	N/C	N/C	JTAG3	Host Mode:TDI (Test Data Input) Target Mode: Test Clock+
7	GND	Ground	JTAG4	Host Mode: TDO (Test DataOutput) Target Mode: Test Clock-
8	+3.3V	3.3 V power	N/C	Not connected
9	N/C	Host Mode: Not connected Target Mode: Test Power On	N/C	Not connected
10	3.3Vaux	3.3 V auxiliary power	+3.3V	3.3 V power
11	N/C	N/C	PERST#	Fundamental reset
Mechanical key				
12	RSVD	Reserved	GND	Ground
13	GND	Ground	REFCLK+	Reference clock (differential pair)
14	PETp0	Transmitter differential pair, Lane 0	REFCLK	
15	PETn0		GND	Ground
16	GND	Ground	PERp0	Receiver differential pair, Lane 0
17	PRSNT2#	Hot-Plug presence detect	PERn0	
18	GND	Ground	GND	Ground
19	PETp1	Transmitter differential pair, Lane 1	RSVD	Reserved
20	PETn1		GND	Ground
21	GND	Ground	PERp1	Receiver differential pair, Lane 1
22	GND	Ground	PERn1	
23	PETp2	Transmitter differential pair, Lane 2	GND	Ground
24	PETn2		GND	Ground
25	GND	Ground	PERp2	Receiver differential pair, Lane 2
26	GND	Ground	PERn2	
27	PETp3	Transmitter differential pair	GND	Ground

28	PETn3	Lane 3	GND	Ground
29	GND	Ground	PERp3	Receiver differential pair, Lane 3
30	RSVD	Reserved	PERn3	
31	PRSNT2#	Hot-Plug presence detect	GND	Ground
32	GND	Ground	RSVD	Reserved

PCI Table 2: Pin-out for the External PCIe Cable Connector

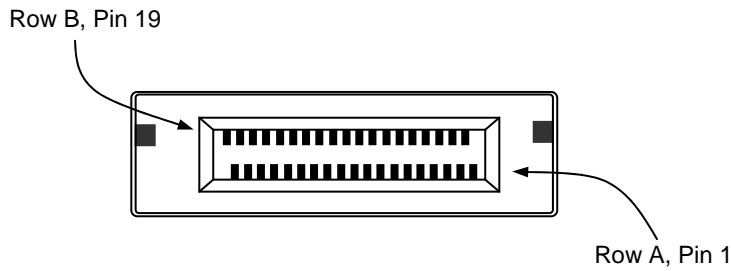
PIN #	Signal	Description	Notes
A1	GND	Ground reference for PCI Express transmitter Lanes	
A2	PETp0	Differential PCI Express transmitter Lane 0 5	5
A3	PETn0	Differential PCI Express transmitter Lane 0 5	5
A4	GND	GND Ground reference for PCI Express transmitter Lanes	
A5	PETp1	Differential PCI Express transmitter Lane 1 5	5
A6	PETn1	Differential PCI Express transmitter Lane 1	5
A7	GND	Ground reference for PCI Express transmitter Lanes	
A8	PETp2	Differential PCI Express transmitter Lane 2	5
A9	PETn2	Differential PCI Express transmitter Lane 2	5
A10	GND	Ground reference for PCI Express transmitter Lanes	
A11	PETp3	Differential PCI Express transmitter Lane 3	5
A12	PETn3	Differential PCI Express transmitter Lane 3	5
A13	GND	Ground reference for PCI Express transmitter Lanes	
A14	CREFLK+	Differential 100MHz cable reference clock	
A15	CREFLK	Differential 100MHz cable reference clock	
A16	GND	GND Ground reference for cable reference clock	
A17	SB_RTN	Signal return for single ended sideband signals	
A18	CPRSNT#	Used for detection of whether a cable is installed and the downstream subsystem is powered	
A19	CPWRON	Turns power on / off to slave-type downstream subsystems	
B1	GND	Ground reference for PCI Express receiver Lanes	
B2	PERp0	Differential PCI Express receiver Lane 0	5
B3	PERn0	Differential PCI Express receiver Lane 0	5

B4	GND	Ground reference for PCI Express receiver Lanes	
B5	PERp1	Differential PCI Express receiver Lane 1	5
B6	PERn1	Differential PCI Express receiver Lane 1	5
B7	GND	Ground reference for PCI Express receiver Lanes	
B8	PERp2	Differential PCI Express receiver Lane 2	5
B9	PERn2	Differential PCI Express receiver Lane 2	5
B10	GND	Ground reference for PCI Express receiver Lanes	
B11	PERp3	Differential PCI Express receiver Lane 3	5
B12	PERn3	Differential PCI Express receiver Lane 3	5
B13	GND	Ground reference for PCI Express receiver Lanes	
B14	PWR	+3.3VCable power	
B15	PWR	+3.3VCable Power	
B16	PWR RTN	Cable power return	
B17	PWR RTN	Cable power return	
B18	CWAKE#	Power management signal for wakeup events (optional)	1, 3
B19	CPERST#	Cable PERST#	

Notes:

- 1 Optional signals that are not implemented are to be left as no connects on the board side connector.
- 2 Reserved signals must be left as no connects on the board side connector.
- 3 Although support of CWAKE# is optional from the board side connector perspective, an allocated wire is mandated for the cable assembly.
- 4 Board side pin-out on both sides of the Link is identical. The cable assembly incorporates a null modem for the PCIe transmit and receive pairs.

PCI Express x4 Connector Pin Assignment



PIN- out for the PCIe x4 Cable

Pin #	Cable Side 1		Cable Side 2	Pin #
A1 A4 A7 A10 A13 A16 B1 B4 B7 B10 B13	GND	Drain Wires	GND	A1 A4 A7 A10 A13 A16 B1 B4 B7 B10 B13
A2	PETp0	Differential Pair	PERp0	B2
A3	PETn0		PERn0	B3
A5	PETp1	Differential Pair	PERp1	B5
A6	PETn1		PERn1	B6

Pin #	Cable Side 1		Cable Side 2	Pin #
A8	PETp2	Differential Pair	PERp2	B8
A9	PETn2		PERn2	B9
A11	PETp3	Differential Pair	PERp3	B11
A12	PETn3		PERn3	B12
A14	CREFLK+	Differential Pair	CREFLK+	A14
A15	CREFLK-		CREFLK-	A15
A17	SB_RTN	Hook-up Wire	SB_RTN	A17
A18	CPRSNT#	Hook-up Wire	CPRSNT#	A18
A19	CPWRON	Hook-up Wire	CPWRON	A19
B2	PERp0	Differential Pair	PETp0	A2
B3	PERn0		PETn0	A3
B5	PERp1	Differential Pair	PETp1	A5
B6	PERn1		PETn1	A6
B8	PERp2	Differential Pair	PETp2	A8
B9	PERn2		PETn2	A9
B11	PERp3	Differential Pair	PETp3	A11
B12	PERn3		PETn3	A12
B14	PWR	NC	PWR	B14
B15	PWR	NC	PWR	B15
B16	PWR_RTN	NC	PWR_RTN	B16
B17	PWR_RTN	NC	PWR_RTN	B17
B18	CWAKE#	Hook-up Wire	CWAKE#	B18
B19	CPERST#	Hook-up Wire	CPERST#	B19
Backshell	Chassis Ground	Overall Cable Braid	Chassis Ground	Backshell

4.d. Signal Descriptions

PETp(x)	PCI Express Transmit Positive signal of (x) pair.
PETn(x)	PCI Express Transmit Negative signal of (x) pair.
PERp(x)	PCI Express Receive Positive signal of (x) pair.
PERn(x)	PCI Express Receive Negative signal of (x) pair.
CREFCLK+/-	Cable REFerence CLock: Provides a reference clock from the host system to the remote system.
SB_RTN	Side Band ReTurN: return path for single ended signals from remote systems.
CPRSNT#	Cable PReSeNT: Indicates the presence of a device beyond the cable.
PWR	PoWeR: Provides local power for in-cable redriver circuits. Only needed on long cables. (Power does not go across the cable.)
PWR_RTN	PoWeR ReTurN: Provides local power return path for PWR pins.
CWAKE#	Cable WAKE
CPERST#	Cable PCI Express Reset

5. Ordering Information

OSS-PCIe-HIB2-x4-H (Host)
OSS-PCIe-HIB2-x4-T (Target)

Other products you may need;

OSS-PCIe-HIB25-x4-H – PCIe x4 Gen 2 host cable adapter installs in a x4, x8, x16 expansion slot of a host system to extend the host PCIe bus to an expansion system or PCIe device.

OSS-PCIe-HIB25-x4-T – PCIe x4 Gen 2 target cable is only used with the OSS 2-slot PCIe backplane to add a single PCIe card to a host.

OSS-PCIe-HIB25-x8-H – PCIe x8 Gen 2 host cable adapter installs in a x8 or x16 expansion slot of a host system to extend the host PCIe bus to an expansion system or PCIe device.

OSS-PCIe-HIB25-x8-T – PCIe x8 Gen 2 target cable adapter is only used with the OSS 2-slot PCIe backplane to add a single PCIe card to a host.

OSS-PCIe-HIB25-x16-H – PCIe x16 Gen 2 host cable adapter installs in a x16 expansion slot of a host system to extend the host PCIe bus to an expansion system or PCIe device.

OSS-PCIe-HIB25-x16-T – PCIe x16 Gen 2 target cable adapter is only used with the OSS 2-slot PCIe backplane (OSS-PCIeBP-2010, P.34) to add an additional PCIe slot to any device.

OSS-XMC-HIB25-x8 – XMC PCIe x8 Gen 2 host cable adapter installs in an XMC connector on a host carrier board and cables to a PCIe downstream device or expansion chassis.

OSS-PCIe-HIB35-x4 – PCIe x4 Gen 2 cable adapter with PCIe switch (including NT port and DMA controller) operates in upstream or downstream mode with DIP switch setting change.

OSS-SHB-ELB-x4/x8-2.0 – PCIe x8 or x4 Gen 2 expansion link board installs in SHBe slot of a PCIe Gen 2 backplane, allowing either x8 or x4 cable inputs from upstream host system.

OSS-PCIe-CA-x1/x4 – PCIe cable adapter fits into slot or stand-alone, converts PCIe x1 cable to PCIe x4 cable.

OSS-PCIe-CA-x4/x8 – PCIe cable adapter fits into slot or stand alone, converts PCIe x4 cable to PCIe x8 cable.

OSS-PCIe-CBL-x4-1M – 1 meter PCIe x4 cable with PCIe x4 connectors.

OSS-PCIe-CBL-x4-2M – 2 meter PCIe x4 cable with PCIe x4 connectors.

OSS-PCIe-CBL-x4-3M – 3 meter PCIe x4 cable with PCIe x4 connectors.

OSS-PCIe-CBL-x4-5M – 5 meter PCIe x4 cable with PCIe x4 connectors.

OSS-PCIe-CBL-x4-7M – 7 meter PCIe x4 cable with PCIe x4 connectors.

OSS-PCIe-CBL-ACT-x4-10M – 10 meter active optical cable with PCIe x4 connectors.

OSS-PCIe-CBL-ACT-x4-100M – 100 meter active optical cable with PCIe x4 connectors.