



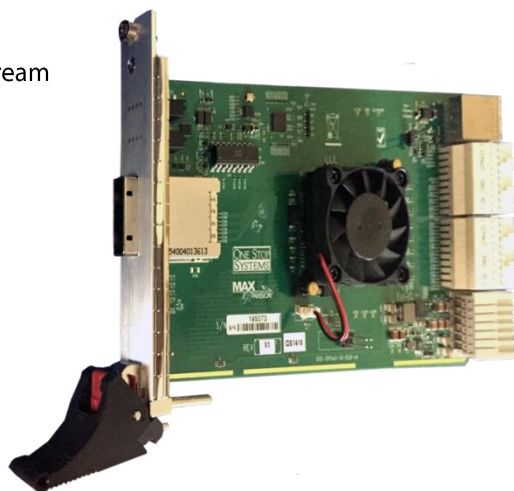
Expansion Link Board (ELB)

3U Gen 3 PCIe expansion link board installs in a 3U PCIe system slot of a downstream PCIe expansion system.

PN: OSS-PCIe3-3U-ELB-x4

Features

- PICMG EXP.0 Rev 1.0 system slot compliant
- x4 PCI Express PCI-SIG compliant cable Uplink
- Configurable 4x4 or 2x8 PCI Express links to the backplane
- 40Gbps cable bandwidth
- 3U or 6U form factor



Specifications

Form Factor:	PICMG EXP.0 system slot compliant. Single slot.
Dimensions:	(H x L) 3.937 x 6.299 in (100 x 161 mm), 1 slot wide
Switch:	PLX PEX8733 32 lane enhanced switch including <ul style="list-style-type: none"> • Configurable lane configuration based on PXIe or PCIe backplane • Spread spectrum clock isolation • DMA controller
External Interface:	One x4 PCIe cable input <ul style="list-style-type: none"> • PCI-SIG PCI Express Cable Rev 2.0 compliant
Downstream Interface:	16 lanes of PCI Express on the backplane connectors. The lanes form two or four links to the backplane through the XP2 and XP3 connectors per the PICMG EXP.0 specification. The following link modes are automatically selected by the backplane as follows: <ul style="list-style-type: none"> • Two link mode: 2 x8 links (per backplane capabilities) • Four link mode: Four x4 PCIe links
Front Panel Connector:	Molex 75586-0010
Front Panel Indicators:	4 backplane link-active indicators (green) 2 or 4 link mode + cable link (green)
Power Consumption:	10W typical +12V @ .5, 3.3V @ 1.25A, 5Vaux @ 2.5mA
Temperature Range:	0° to 50°C
Relative Humidity:	10 to 90% non-condensing
Agency/Compliances:	Designed to Meet: FCC Class A CE RoHS 6 of 6