



## CPCI Hybrid Bridge Board (HYB)

CPCIe-to-CPCI bridge board bridges between CPCIe slots and CPCI slots installs in the hybrid bridge slot; of an OSS-hybrid backplane.

PN: OSS-CPCIe-3U-IOe-HYB

### Features

- Bridges between CPCIe and CPCI slots
- Requires hybrid slot on backplane
- Available in 3U and 6U



## Specifications

<b>Form Factor:</b>	3U or 6U Compact PCI Express, PICMG EXP.0, Single Slot
<b>Slot Type:</b>	PICMG EXP.0 busseed hybrid slot
<b>Board Dimensions:</b>	3.937" H x 6.299" L x .62in W (100 x 160 x 15.7 mm)
<b>Bridge:</b>	PLX PEX 8114
<b>Backplane Interfaces:</b>	One PCIe x4 link on Advanced Differential Fabric (ADF) connector XJ3 One 32-Bit/33 or 66 MHz PCI bus on HM connector J1 & power/additional PCI signals
<b>Front Panel Indicators:</b>	Bank of 4 PCIe lane active indicators (green)
<b>CPCI Interfaces:</b>	32-bit at 66 or 33 MHz 3.3V I/O Internal arbiter supports up to 4 external masters 4 CPCI slots (up to 4 PCI clocks/bus requests/bus grants)
<b>Power Consumption:</b>	3.3V @ 2A
<b>Temperature Range:</b>	0° to 55°C (32° to 122°F)
<b>Relative Humidity:</b>	5 to 90% non-condensing
<b>Shock:</b>	30g acceleration peak (11 ms pulse)
<b>Vibration:</b>	5-17 Hz 0.5" double amplitude displacement; 7-2000Hz, 1.5g acceleration
<b>Agency Compliance:</b>	FCC Class B CE RoHS