

2U Compute Accelerator with Intel Xeon Phi Coprocessors

The CA4001 Compute Accelerator with four Intel Xeon Phi Coprocessors is employed in a variety of HPC applications including oil and gas exploration and financial services. Completely integrated with the Coprocessors most suited for a specific application, it's easy installation and tested reliability makes it superior to alternative products. The CA4001 occupies only 2U of rack space and connects directly to one or two host server(s) through the latest technology PCIe x16 Gen3 connection.

PN: OSS-PCIe3-2U

Features

- 2U High
- One or Two Rear Panel PCIe x16 Gen3 Interfaces
- Remote System Monitoring Capability; monitor fans, temperature and voltages
- Dual Redundant 1620-watt Power Supplies
- Superior Cooling with Four Temperature Controlled Fans
- Choice of 1 to 4 Intel Xeon Phi Coprocessors



Specifications

Enclosure	
Dimensions	3.5"H x 17"W x 22.25" D Removable front bezel with air filter Front Panel LEDs
Capacity	Up to 4 Intel Xeon Phi Coprocessors
Power Supply	Dual redundant 850W power supply (removable)
Expansion	One or two PCIe x16 1-meter cable(s) One or two PCIe x16 Gen3 cable adapter(s)
Cooling	Four 132 CFM fans (removable)
Operating Environment	1-35°C 10-90% relative humidity 0-10,000 feet above sea level
Storing Environment	-40 to 85°C Any relative humidity 0-50,000 feet above sea level
Agency Compliance	Pending: FCC Class A CE RoHS

Intel Xeon Phi Coprocessors			
Model	3120P	5110P	7120P
Peak Double Precision Performance	1.003 teraflops	1.011 teraflops	1.208 teraflops
Board TDP	300W	225W	300W
# of Cores	57	60	61
Core Freq.	1.1 GHz	1.05 GHz	1.24 GHz
Memory Capacity	6GB	8GB	16GB
Memory Bandwidth	240GB/s	320GB/s	352GB/s
Computing Applications	Monte Carlo, Black-Scholes, HPL, LifeSC	STREAM, ray-tracing, RTM	Seismic Imaging Processing, Molecular Dynamics, WRF
Architecture Features	Maximum Value	Power Efficient	Intel Turbo Boost 1.0 1.33GHz

For a list of qualified servers, go to <http://www.onestopsystems.com/hpc/2u-compute-accelerator-intel-xeon-phi-coprocessors>